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Tutorial: A Journey to Optical Computing: From Physics Fundamentals to Hardware-Software Co-Design, Automation, and Application

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Outline of Tutorial

 Tutorial I: Fundamentals of Optical Computing and Integrated Photonics for High-Performance Digital Logic and Efficient Machine Learning
Jiaqi Gu, Chenghao Feng (UT Austin, Arizona State University)

 Tutorial II: LightRidge: An End-to-end Agile Design Framework for Diffractive Optical Neural Networks
Yingjie Li, Cunxi Yu (University of Utah)

 Tutorial III: Topology and Physical Layout Optimization of Photonic Networks-on-Chip and PIC Variation Analysis
Ulf Schlichtmann (Technical University of Munich)

Tutorial IV: Integrated Programmable Photonic Circuits
Zhengqi Gao (MIT)





Tutorial I: Fundamentals of Optical Computing and Integrated Photonics for High-Performance Digital Logic and Efficient Machine Learning

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Presenters: Jiaqi Gu^{1,2}, Chenghao Feng^{1,3}

Contributors: Hanqing Zhu¹, Zhoufeng Ying¹, Zheng Zhao¹, Ray T. Chen¹, David Z. Pan¹

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Outline of Tutorial I

Introduction to Optical Computing

Design and Demonstration of Electronic-Photonic Digital Computing

Analog Photonic Computing for Optical Neural Networks
Coherent Photonic Tensor Core
Incoherent Photonic Tensor Core



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The Timeline of Optical Computing





Digital vs. Analog Photonic Computing



Photonic Computing Chips

Evolve from <u>electronics</u> to <u>integrated photonics</u>





Source: Mitchell A. Nahmias, Bhavin J. Shastri, Alexander N. Tait, Thomas Ferreira de Lima and Paul R. Prucnal, "Neuromorphic photonics," Optics & Photonics News, Jan 2018.

Electrical Computing vs Photonic Computing





Application Potentials of Photonic Computing

Ultra-fast, efficient digital control / ALU
Energy-efficient, real-time machine intelligence

Fast edge/mobile processing

High-throughput datacenter processing



Smart commun. network, distributed computing





Scientific comp., optimization, bio / material..





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Progress in Optical Digital Computing





WDM-based Electronic-Photonic Computing Circuits





Photonic-Electronic Arithmetic Logic Unit (ALU)

From electrical ALU to <u>high-speed</u> and <u>energy-efficient</u> photonic-electronic ALU
We demonstrate 20Gb/s photonic-electronic digital computing chips
For general-purpose digital computing





WDM-based Electronic Photonic Carry-Select Adder

Replace electrical path (carry chain) to optical path











Advantages of Using Optics to Implement Additions





Chip Layout of the Electronic Photonic Full Adder





Comparison with the State-of-the-art Transistors

Compared to 32 nm / 7nm (scaling) from Intel
4× faster (20 GHz vs 5 GHz)

 $>10\times$ more energy-efficient





WDM-based Photonic-Electronic Unsigned Comparator



$$C: A < B?$$
$$Z: A = B?$$

Truth table			
С	Z	Result	
0	1	A=B	
0	0	A>B	
1	0	A <b< td=""></b<>	



[Feng C. et al., Laser & Photonics Reviews, 2021]



Experimental Results (2-bit unsigned comparator)





WDM-based Electronic-Photonic Switching Network

Applications: decoder, multiplexer, demultiplexer



Schematic of the WDM-based switching network

[Feng C. et al., Nanophotonics, 2020]



Experimental Results of the 3-8 Optical Decoder





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Source: https://openai.com/blog/ai-and-compute/

Source: https://spectrum.ieee.org/nvidias-next-gpu-shows-that-transformers-are-transforming-ai

Photonic AI is Booming

Photonic Neural Network Trends in Academia



Foundry / EPDA Support in Industry



Electronic-Photonic Design Automation Tools



PDK / Tape-out / Packaging Support





Photonic AI Computing Basics

Principle: light modulation, interference, photodetection

Good at <u>ultra-fast</u>, <u>parallel</u> linear operations in the <u>analog</u> domain

Nonlinear		Computing Primitives	Photonic Implementation
Absorber		Scalar Multiply	Light Modulation
		$y = a \cdot x$	$x \not \longrightarrow \not y$
		2×2 Unitary Matrix Multiply	Mach-Zehnder Interferometer (MZI)
Optical		$\boldsymbol{y} = \boldsymbol{R}(2) \times \boldsymbol{x}$	$x_1 \leftrightarrow \phi \rightarrow y_1$
	В	$\boldsymbol{R}(2) = \begin{pmatrix} \cos\phi & -\sin\phi \\ \sin\phi & \cos\phi \end{pmatrix}$	$\begin{array}{c} x_2 \\ \end{array} \\ \hline \end{array} \\ \begin{array}{c} x_1 \\ \end{array} \\ \hline \end{array} \\ \begin{array}{c} x_2 \\ \end{array} \\ \hline \end{array} \\ \\ \hline \end{array} \\ \\ \\ \hline \end{array} \\ \\ \\ \end{array} \\ \hline \end{array} \\ \\ \\ \\$
ewitching micro-mimor ITO electrode		Matrix-Vector Multiply (MVM)	Photonic Tensor Core (PTC)
E/O		$y = W \times x$	$W(\Phi) = U\Sigma V^* $
	(d)		x (light in)
•••			V^* Σ U

One-shot computing at speed-of-light!



Photonic Tensor Core (PTC) Categories

Encoding

Coherent $|x|e^{j\phi(x)}$: magnitude + phase

MZM 1
$$\theta$$
 ϕ $\cos\theta \cdot e^{j\phi}$

• MVM principle



Matrix Expressivity







Incoherent |x|: magnitude-only





Coherent ONN Architectures

• Encoding: $|x|e^{j\phi(x)}$ magnitude + phase • Computing: interference (indirect)

○ MZI array [Shen+, Nat. Photon'17]

• Singular value decomposition $W = U\Sigma V^*$ • Phase decomposition

$$U(N) = D \prod_{i=N}^{2} \prod_{j=1}^{i-1} R_{ij}(\phi_{ij})$$
$$R(2) = \begin{pmatrix} \cos \phi & -\sin \phi \\ \sin \phi & \cos \phi \end{pmatrix}$$

Universal linear units for arbitrary matrices





 \mathbf{V}^*

N(N-1)/2 MZIs

Σ

II

N(N-1)/2 MZIs

Universal vs. Specialized Photonic Tensor Cores





Specialized Coherent ONN Architectures

 \circ Leverage the matrix redundancy \rightarrow reduce hardware cost \rightarrow subspace linear





J. Gu, Z. Zhao, C. Feng, M. Liu, R.T. Chen, D.Z. Pan, "Towards Area-Efficient Optical Neural Networks: An FFT-based Architecture," ACM/IEEE ASP-DAC, 2020. Best Paper Award

Butterfly-style Photonic Tensor Core

Efficient circulant matrix multiplication in Fourier domain





J. Gu, Z. Zhao, C. Feng, M. Liu, R.T. Chen, D.Z. Pan, "Towards Area-Efficient Optical Neural Networks: An FFT-based Architecture," ACM/IEEE ASP-DAC, 2020. Best Paper Award

Butterfly Photonic Mesh for Circulant MVM





J. Gu, Z. Zhao, C. Feng, M. Liu, R.T. Chen, D.Z. Pan, "Towards Area-Efficient Optical Neural Networks: An FFT-based Architecture," ACM/IEEE ASP-DAC, 2020. Best Paper Award

Photonic Neural Chip Tapeout & Demonstration





C. Feng*, J. Gu* (co-first), H. Zhu, Z. Ying, Z. Zhao, D.Z. Pan, R.T. Chen, "A compact butterfly-style silicon photonicelectronic neural chip for hardware-efficient deep learning", ACS Photonics, Nov. 30, 2022.

Evaluate on ML Tasks & Efficiency





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Incoherent ONN Architectures

 \circ Encoding: |x|

Computing: Multi-wavelength modulation + photodetection

Microring resonator (MRR) weight bank

 $y = w_1 x_1 + w_2 x_2 + w_3 x_3 + \dots + w_n x_n$ • All-pass MRR weight bank

 $\circ w_i = a_i \in [0, 1]$

Add-drop MRR weight bank

$$o w_i = a_i - (1 - a_i) = 2a_i - 1 \in [-1, 1]$$

Compact in size
Can we do it better?
Rottlonock by 1 Op/dovi

Bottleneck by 1 Op/device







Customized Incoherent ONN Architectures

(()))

Multi-operand optical neuron (MOON):

Single-device to implement vector-vector multiplications (beyond 1 OP/device)

 \odot Built-in non-linear transfer function $T(\cdot)$:

$$y_i = T\left(\Sigma_j\left(\phi_j(V_j)\right)\right)$$
$$V_j = w_j \cdot x_j \text{ or } \phi_j = w_j \cdot x_j \dots$$

• Weight (w) and input (x) encoding:

 $k \times$ higher compute density at the same cost



(a) Multi-operand microring





Fixed weight

(MOON) Multi-Operand Ring Resonators

• MORR: *k*-segment controllers on one micro-ring • Single-device length-*k* vector dot-product Round-trip phase: $\phi \propto \sum_{i=0}^{k-1} w_i x_i^2$

Built-in nonlinearity

• Half-Tanh-like nonlinear activation $f(\cdot) \in (0, 1)$ • Tunable smoothness (r, a) $|r = a e^{-j\phi}|^2$

$$f(\phi) = \left| \frac{r - a e^{-j\phi}}{1 - ra e^{-j\phi}} \right|$$

$$OUT = f(\phi) \cdot in \propto f\left(\sum_{i=0}^{k-1} w_i x_i^2\right) \cdot IN$$







MORR-based ONN: SqueezeLight [Gu+, DATE'21, TCAD'22]





 $\mathbf{y} = \mathbf{x}^T \mathbf{w}_+ - \mathbf{x}^T \mathbf{w}_-$

Cross-layer Scalability Evaluation

- Compare with SoTA MRR-ONNs on MNIST, FMNIST, CIFAR-10
- 23×-32× less device usage
- \circ 8× fewer wavelength usage
- MORR array vs MRR array
 - o w/ same area budget
 - \circ **5.3**× higher TOPS/mm²
 - 9.8× higher TOPS/W
 - o 63.5% system energy reduction



- Good expressivity & training scalability
- Robust to crosstalk/noises with special robustness optimization

(MOON) Multi-Operand MZI

 \circ Partition MZI controllers into k segments

- Dot-product + nonlinearity: $y = cos(\sum_i w_i x_i)$
- Scale up to larger vectors with WDM
- $_{\odot}$ Fewer cascaded device \rightarrow lower insertion loss and delay

Same power/area as a single MZI



of operands k depends on controlling/fabrication precision and chip layout: 4/8/16/...



MOMZI Chip Layout (4-op MOMZI)





Evaluation Results

Robust output with small noises (~0.5%)
 ~86% measured acc on 4-layer CNN SVHN
 4-bit voltage control precision







Performance Analysis

>100 dB smaller insertion loss and 7.2× smaller footprint





Results based on AIM photonics PDK, For MZI-ONN, we use thermo-optical MZI switch for weight programming

Open-Source Photonic AI: TorchONN



Torch

ONN

Photonic AI Library TorchONN

Construction: customized optical Conv/Linear layers

Modeling of various devices

- PCM, MZI, MRR, MORR, ...
- Support various tensor core designs
 - MRR weight bank / MORR / MZI / FFT array...
 - CUDA backend for customized operators...

Mapping: convert from electrical to optical

Decomposition or optimization-based map

Co-design infrastructure

- Device quantization & QAT
- Noise injection & NAT
- Circuit pruning & PAT
- On-chip training support
 - Zeroth-order optimization

Circuit topology search (SuperMesh)





A PyTorch Library for Photonic Integrated Circuit Simulation and Photonic AI Computing

import torchonn as onn	. .
<pre>from torchonn.models import ONNBaseModel</pre>	🗋initpy
<pre>class ONNModel(ONNBaseModel):</pre>	🗋 base_layer.py
<pre>definit(self, device=torch.device("cuda:0)): super(). init (device=device)</pre>	fftonn_conv2d.py
<pre>self.conv = onn.layers.MZIBlockConv2d(</pre>	fftonn_linear.py
<pre>in_channels=1, out channels=8,</pre>	morr_conv2d.py
kernel_size=3,	🗋 morr_linear.py
stride=1,	mzi_conv2d.py
dilation=1,	mzi_linear.py
<pre>bias=True, miniblock=4</pre>	pcm_conv2d.py
mode="usv",	pcm_linear.py
<pre>decompose_alg="clements",</pre>	super_conv2d.py
pnotodetect=Irue, device=device,	🗋 super_linear.py



super mesh.py



Tutorial II: LightRidge: An End-to-end Agile Design Framework for Diffractive Optical Neural Networks

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Yingjie Li, Ruiyang Chen, Minhan Lou, Berardi Sensale-Rodriguez, Weilu Gao, **Cunxi Yu**

University of Utah

New Trends of Computing

- Al's impacts in hardware system design
 - The raise of domain-specific computing
 - Beaten the trend of *Moore's Law* (R.I.P Dr. Moore)
 - Doubling every **3.5 months** (18 months)

















- A Computer Engineering journey to Optical Al System
 - Challenge 1: Cross-disciplinary domain knowledge barriers







- A Computer Engineering journey to Optical Al System
 - Challenge 1: Cross-disciplinary domain knowledge barriers
 - Challenge 2: Lacks high-performance infrastructures for programming, modeling, training, exploration, fabrication, etc.







- A Computer Engineering journey to Optical Al System
 - Challenge 1: Cross-disciplinary domain knowledge barriers
 - Challenge 2: Lacks high-performance infrastructures for programming, modeling, training, exploration, fabrication, etc.
 - Challenge 3: Limited studies of physics-to-system co-design to enable seamless design-to-deployment



Numerical Emulation

Physical Measurement





- A Computer Engineering journey to Diffractive Optical Neural Networks
 - Challenge 1: Cross-disciplinary domain knowledge barriers
 - **Challenge 2:** Lacks high-performance infrastructures for programming, modeling, training, exploration, fabrication, etc.
 - Challenge 3: Limited studies of physics-to-system co-design to enable seamless design-to-deployment









Input	Images (Real)	Light (Complex)
Operator	Conv, Dense, Pool,	Light Diffraction and Phase Mod
Propagation	Digital Computing	Light Propagation (Complex)
Output	Digital Output (Real)	Light Intensity (CPLEX-2-Real)



Lin, Xing, et al. "All-optical machine learning using diffractive deep neural networks." *Science* 361.6406 (2018): 1004-1008.





DiffMod
$$(X_c(x, y), \theta_0) = iFFT_{2D} (FFT_{2D}(X_c(x, y)) \times FFT_{2D}(h(x, y, z)))$$



Lin, Xing, et al. "All-optical machine learning using diffractive deep neural networks." *Science* 361.6406 (2018): 1004-1008.

(1) Light Diffraction







For example, 3-layer forward function:

 $I(X_c, \theta) = DiffMod (DiffMod (DiffMod (X_c(x, y), \theta_0), \theta_1), \theta_2)$







Overview of LightRidge Framework





Yingjie Li, Ruiyang Chen, Minhan Lou, Berardi Sensale-Rodriguez, Weilu Gao and **Cunxi Yu**. "*LightRidge: An Open-source Compiler Framework for Diffractive Optical ML Architectures.*" Workshop on Open-Source Computer Architecture Research (OSCAR) held in conjunction with ISCA (ISCA'49)



Overview of LightRidge Framework





LightRidge API Example: DiffractiveLayer()



- Output: :math:`(*)`. Output is of the same shape as input
- def __init__(self, phase_func, intensity_func, wavelength,

```
pixel_size, resolution, distance, amplitude_factor,
    name, approx=lr.kernel.Fresnel, phase_mod=True):
    super(DiffractiveLayer, self).__init__()
```





LightRidge API Example: Forward Function







Training – Example of Classification



- Training via Backprop works!
 - Fully tensorized and differentiable physics kernels (Autograd)
 - Customizable loss w.r.t applications
 - e.g., classification, segmentation, etc.

Diffraction intensity pattern captured





Miscorrelation in Experimental Measurements









Co-design Formulation

Discrete "trainable" parameters

Discrete parameters directly selects voltage index

Loss function remains unchanged





Gumbel-Softmax



Co-design Formulation







Domain-specific Complex Regularization

Discrete training via Gumbel-Softmax







Domain-specific Complex Regularization

Regularization is a new hyperparameter

- Varies for different wavelength, depth, and distance
- Tuning needs to combine Gumbel-Softmax temperature schedule





Li, Yingjie, Ruiyang Chen, Weilu Gao, and **Cunxi Yu**. "Physics-aware Differentiable Discrete Codesign for Diffractive Optical Neural Networks." In *Proceedings of the 41st IEEE/ACM International Conference on Computer-Aided Design (ICCAD'22)*. 2022



Comparisons with quantization methods

 Comparisons trained with a fitted continuous curve from a multi-polynomial regression model

	Post-training quantization (PTQ)	Quantization-aware training (QAT)	Weights sharing quantization (WSQ)
Pre-trained model (float32)	X	X	\checkmark
Quantization method	Round after training	Hardware-aware training loss with minibatch clipping	Weights sharing with KMeans clustering





Comparisons with quantization methods



file.csv includes amplitude/phase response in two rows
SLM1_amp, SLM1_phase, ... = lr.utils.load_device([slm1.csv, slm2.csv,])
plug-in in the layer definition
lr.layer.DiffractiveLayer(SLM1_phase, SLM1_amp, wavelength, ...)
lr.layer.DiffractiveLayer(SLM2_phase, SLM2_amp, wavelength, ...)



Li, Yingjie, Ruiyang Chen, Weilu Gao, and **Cunxi Yu**. "Physics-aware Differentiable Discrete Codesign for Diffractive Optical Neural Networks." In *Proceedings of the 41st IEEE/ACM International Conference on Computer-Aided Design (ICCAD'22)*. 2022


Experiments – Visible Range



- Training and hardware setups
 - 10 min training on RTX 3090 Ti and straight out-of-box deployment
 - 98% accuracy in experimental evaluation on MNIST-10
 - Match LightRidge emulation results



Li, Yingjie, Ruiyang Chen, Weilu Gao, and **Cunxi Yu**. "Physics-aware Differentiable Discrete Codesign for Diffractive Optical Neural Networks." In *Proceedings of the 41st IEEE/ACM International Conference on Computer-Aided Design (ICCAD'22)*. 2022



Experimental Energy Efficiency

- FPS/Watt at inference
 - Batch = 1
 - 3 orders vs GPPs
 - 50X vs XPU
 - CNNs/MLPs acc = 0.99
 - DONNs = 0.98
- Can be further optimized with monolithic fabrication and advance setups



APC Metered Rack Supply PDU (CPU/GPU/DONNs measurement)





Experiments – THz Range





- THz hardware setups
 - Laser source 0.3 THz
 - 3D printed diffractive layers
 - Pixel dimension 0.5 mm
 - 93% accuracy in MNIST-10
 - Physical sparsity







Lou, Minhan, Yingjie Li, **Cunxi Yu**, Berardi Sensale-Rodriguez, and Weilu Gao. "Effects of interlayer reflection and interpixel interaction in diffractive optical neural networks." *Optics Letters 48, no. 2 (2023): 219-222.* Yingjie Li*, Shanglin Zhou*, Minhan Lou, Weilu Gao, Caiwen Ding, **Cunxi Yu**. "Physics-aware Roughness Optimization for Diffractive Optical Neural Networks". *Design Automation Conference (DAC'23)*



LightRidge Runtime Speedups

- LightRidge offers orders of magnitude speedups
 - Baseline: LightPipes(2021) and SOTAs [Science'18, Nature Photonics'21]
 - SOTAs reported 3-4 days training time for 5-layer DONNs
- Speedups breakdown
 - DiffractMod are the most critical
 - Deployment of cuFFTC2C and cache planning on *h*







Advanced Architecture – All-Optical Segmentation





Advanced Architecture – All-Optical Segmentation

- Preliminary of all-optical autonomous driving
 - In-door lane following
 - Same architecture as segmentation task



DONNs





Advanced Architecture – All-Optical Segmentation

- Preliminary of all-optical autonomous driving
 - Out-door autonomous driving
 - University campus road (summer)
 - Same architecture as segmentation task









Adversaries of Light



The space of the adversaries in DONNs

Attack Types	HW System	Numerical
Real	Amplitude attack	$(A + \mathbf{p})e^{i\theta} = (A + \mathbf{p})\cos\theta + i \cdot (A + \mathbf{p})\sin\theta$
Complex	Phase attack	$Ae^{i(\theta+p)} = A\cos(\theta+p) + i \cdot A\sin(\theta+p)$
Adversarial Perturbation p		



Yingjie Li, and Cunxi Yu. "Physical Adversarial Attacks of Diffractive Deep Neural Networks." Design Automation Conference (DAC'21)

Adversaries of Light



- Domain-specific generation of adversarial examples
 - Restricted space w.r.t physics meanings
 - Perturbation engineering needs to be considered in the attack phase
- C-FGSM: Complex fast-gradient-signed-method
 - Gumbel-Softmax guided co-design and perturbation engineering





Evaluations of C-FGSM







Physical Experimental Validation



Vulnerability exist and experimentally demonstrated

Natural counter-measurements - the miscorrelation and device noise





Chen, Ruiyang*, Yingjie Li*, Minhan Lou, Jichao Fan, Yingheng Tang, Berardi Sensale-Rodriguez, **Cunxi Yu**, and Weilu Gao. "Physics-Aware Machine Learning and Adversarial Attack in Complex-Valued Reconfigurable Diffractive All-Optical Neural Network." *Laser & Photonics Reviews* (2022).



Other Features

ML-assisted DSE



Advanced Architectures & Multi-task Learning





Monolithic Integration





Yingjie Li, Ruiyang Chen, Minhan Lou, Berardi Sensale-Rodriguez, Weilu Gao and **Cunxi Yu**. "LightRidge: An End-to-end Agile Design Framework for Diffractive Optical Neural Networks." ASPLOS'24

Conclusions

• A Computer Engineering journey to Diffractive Optical Neural Networks







Research Group



PI Dr. Cunxi Yu



Yingjie Li PhD (S20 -) (at DAC'23)



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Co-advised PhD Dr. Walter Lau Neto



BS/MS (thesis) Tara Zamani



NSF REU Nicolas Taylor







Tutorial III: Topology and physical layout optimization of photonic networks-on-chip and PIC variation analysis

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Ulf Schlichtmann,

Technical University of Munich



Topology and physical layout optimization of photonic networks-on-chip



WRONOC – Wavelength-Routed ONoC WRONoC Working Mechanism

- Usage of microring resonators (MRRs) for multiplexing and demultiplexing
- Dedicated signal path determined in design phase for each tuple (initiator, target, wavelength)
- Main constraint: No path overlap between signals with the same wavelength



WRONoC Pros and Cons

• Advantages:

- No control resource
- No scheduling effort
- No congestion control
- No signal path construction → no uncertain signal delay
- Disadvantages:
 - Extensive usage of MRRs (1 MRR serves constant #paths) →
 Scalability issue! → suitable for application-specific usage → need design optimization to save resources



WRONoC Design Features

Topological features:

- Waveguide connection structure
- MRR topological locations
- MRR resonant wavelengths
- Signal wavelength assignment
- Signal path routing
- All these need to be done during the design phase

→ Challenges of efficiency! & beyond human capability!

Physical design features:

- Waveguide routing
- MRR placement

Manual topology



Manual layout

м1	
M2	

Sources:

1) Engineering a Bandwidth-Scalable Optical Layer for a 3D Multi-core Processor with Awareness of Layout Constraints, NOCS'12, Luca Ramini et al.

WRONoC Research at TUM — Since 2018

- Router design and synthesis:
 - Topology synthesis
 - CustomTopo (ICCAD'18)
 - FAST (DATE'21, TCAD'22)
 - Topology design
 - Light (ASP-DAC'21)
 - Physical synthesis
 - ToPro (ICCAD'21)
 - Topology synthesis + physical synthesis
 - PSION (ISPD'19, TCAD'20, ICCAD'20)
- Bandwidth maximization: *MaxBW (ASP-DAC'20)*

WRONoC Research at TUM

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Separate Design Steps

- Topology generation and then physical design
- Advantages:
 - Natural problem partitioning
 - Observable intermediate solution, i.e. topology
 - Fast
- Disadvantages:
 - Node position not considered → long waveguide detours and crossings

automatically-synthesized topology



Sources:

1) CustomTopo: A Topology Generation Method for Application-Specific Wavelength-Routed Optical NoCs, ICCAD'18, Mengchu Li et al.

Topology Synthesis by CustomTopo

Input: communication graph



wavelength for each message determined wavelength for each ADF determined #ADF-sharing structures maximized

Sources:

1) CustomTopo: A Topology Generation Method for Application-Specific Wavelength-Routed Optical NoCs, ICCAD'18, Mengchu Li et al.

Information in the Communication Matrix



Sources:

1) CustomTopo: A Topology Generation Method for Application-Specific Wavelength-Routed Optical NoCs, ICCAD'18, Mengchu Li et al.

Topology Synthesis by CustomTopo



Sources:

1) CustomTopo: A Topology Generation Method for Application-Specific Wavelength-Routed Optical NoCs, ICCAD'18, Mengchu Li et al.

2) PROTON+: A Placement and Routing Tool for 3D Optical Networks-on-Chip with a Single Optical Layer, JETC'15, Anja von Beuningen et al.

Topology Synthesis by FAST

Reduced from Snake topology





Results comparable with

Sources:

- 1) Contrasting Wavelength-Routed Optical NoC Topologies for Power-Efficient 3D-stacked Multicore Processors using Physical-Layer Analysis, DATE'13, Luca Ramini et al.
- 2) FAST: A Fast Automatic Sweeping Topology Customization Method for Application-Specific Wavelength-Routed Optical NoCs, DATE'21, Moyuan Xiao et al.
- 3) Crosstalk-Aware Automatic Topology Customization and Optimization for Wavelength-Routed Optical NoCs, IEEE TCAD'22, Moyuan Xiao et al.

WRONoC Research at TUM

- Router design and synthesis:
 - Topology synthesis
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 - Topology design
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 - PSION (ISPD'19, TCAD'20, ICCAD'20)
- Bandwidth maximization: *MaxBW (ASP-DAC'20)*

Light: $n \times (n - 1)$ WRONoC Router

- Physical-design-aware
- A wide range of signal-to-noise ratio (SNR) distribution — good potential for signal path binding











8×7 Light



Sources:

1) Light: A Scalable and Efficient Wavelength-Routed Optical Networks-On-Chip Topology, ASP-DAC'21, Zhidan Zheng et al.

Light: Results in detail

Number of paths and their SNR values for different WRONoC topologies supporting 32 IP-Cores



WRONoC Research at TUM

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 - Topology synthesis
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 - Topology design
 - Light (ASP-DAC'21)
 - Physical synthesis
 - ToPro (ICCAD'21)
 - Topology synthesis + physical synthesis
 - PSION (ISPD'19, TCAD'20, ICCAD'20)
- Bandwidth maximization: *MaxBW (ASP-DAC'20)*

ToPro: Waveguide Router

- Steps:
 - 1. Project a physical-design-aware topology, e.g. Light, onto the center of the routing plane
 - 2. Route shortest paths
 - 3. Crossing resolution by path pushing
- Zero-crossing waveguide routing from router to nodes
- Minimize insertion loss & Maximize SNR

Router rotation and flip



Crossing resolution by path pushing



Sources:

1) ToPro: A Topology Projector and Waveguide Router for Wavelength-Routed Optical Networks-on-Chip, ICCAD'21, Zhidan Zheng et al.

2) Topological routing to maximize routability for package substrate, DAC'08, Shenghua Liu et al.

WRONoC Research at TUM

- Router design and synthesis:
 - Topology synthesis
 - CustomTopo (ICCAD'18)
 - FAST (DATE'21, TCAD'22)
 - Topology design
 - Light (ASP-DAC'21)
 - Physical synthesis
 - ToPro (ICCAD'21)
 - Topology synthesis + physical synthesis
 - PSION (ISPD'19, TCAD'20, ICCAD'20)
- Bandwidth maximization: *MaxBW* (ASP-DAC'20)

PSION: Template-Based Synthesis



Sources:

- 1) PSION: Combining logical topology and physical layout optimization for Wavelength-Routed ONoCs, ISPD'19, Alexandre Truppel et al.
- 2) PSION+: Combining logical topology and physical layout optimization for Wavelength-Routed ONoCs, IEEE TCAD 39(12) 2020, Alexandre Truppel et al.
- 3) PSION 2: Optimizing Physical Layout of Wavelength-Routed ONoCs for Laser Power Reduction, Alexandre Truppel et al.

WRONoC Synthesis by PSION

a "Screen Savor" multimedia application



16 nodes, 22 messages

- Full CM would have 240 messages, 240 MRRs required for Lambda-router
- Here only 27 MRRs are used

WRONoC router synthesized by PSION



Sources:

1) PSION: Combining logical topology and physical layout optimization for Wavelength-Routed ONoCs, ISPD'19, Alexandre Truppel et al.

2) A scalable, non-interfering, synthesizable Network-on-chip monitor — extended version, Microprocessors and Microsystems'13, Antti Alhonen et al.

WRONoC Research at TUM

- Router design and synthesis:
 - Topology synthesis
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- Bandwidth maximization: *MaxBW (ASP-DAC'20)*

Bandwidth Maximization

- Convention: "1-bit communication"
- Periodic transmission spectrum of microring resonators
- Input: a WRONoC topology
- Output: the same topology with maximized communication parallelism

Bandwidth requirement (unit: MB/s) of an MPEG-4 decoder application





Sources:

1) Maximizing the Communication Parallelism for Wavelength-Routed Optical Networks-on-Chips, ASP-DAC'20, Mengchu Li et al.

2) NoC synthesis flow for customized domain specific multiprocessor systems-on-chip, IEEE TPDS 16(2) 2008, Davide Bertozzi et al.
WRONoC Research at TUM — Since 2018

- Router design and synthesis:
 - Topology synthesis
 - CustomTopo (ICCAD'18)
 - FAST (DATE'21, TCAD accepted)
 - Topology design
 - Light (ASP-DAC'21)
 - Physical synthesis
 - ToPro (ICCAD'21)
 - Topology synthesis + physical synthesis
 - PSION (ISPD'19, TCAD'20, ICCAD'20)

Many thanks to the researchers and students working with me: <u>Tsun-Ming Tseng</u>, Mengchu Li, Alexandre Truppel, Zhidan Zheng, Moyuan Xiao and to my collaborators:

- Prof. Davide Bertozzi (University of Ferrara, Italy)
- Dr. Mahdi Tala (University of Ferrara, Italy)
- Prof. Mahdi Nikdast (Colorado State University, USA)
- Bandwidth maximization: *MaxBW (ASP-DAC'20)*



PIC variation analysis

Thanks to

Ying Zhu, Bing Li, Grace Li Zhang (TUM)

and to our collaborators Xunzhao Yin, Cheng Zhuo (Zhejiang), Huaxi Gu (Xidian), Tsung-Yi Ho (CUHK)



Mach-Zehnder Interferometer (MZI)

- Component for light signal transformation
- Behavior of optical signals:
 - Directional coupler (beam splitter): split signal by 50:50; append π/2 in phases of diagonal transmission
 - Phase shifter: thermally controllable phases for programming

transformation matrices of phase shifters

$$\begin{bmatrix} L_1^{\prime c} \\ L_2^{\prime c} \end{bmatrix} = \begin{bmatrix} \frac{1}{\sqrt{2}} & \frac{i}{\sqrt{2}} \\ \frac{i}{\sqrt{2}} & \frac{1}{\sqrt{2}} \end{bmatrix} \begin{bmatrix} e^{i\theta} & 0 \\ 0 & 1 \end{bmatrix} \begin{bmatrix} \frac{1}{\sqrt{2}} & \frac{i}{\sqrt{2}} \\ \frac{i}{\sqrt{2}} & \frac{1}{\sqrt{2}} \end{bmatrix} \begin{bmatrix} e^{i\phi} & 0 \\ 0 & 1 \end{bmatrix} \begin{bmatrix} L_1^c \\ L_2^c \end{bmatrix} = ie^{i\theta/2} \begin{bmatrix} e^{i\phi}\sin\frac{\theta}{2} & \cos\frac{\theta}{2} \\ e^{i\phi}\cos\frac{\theta}{2} & -\sin\frac{\theta}{2} \end{bmatrix} \begin{bmatrix} L_1^c \\ L_2^c \end{bmatrix} = \mathbf{T} \begin{bmatrix} L_1^c \\ L_2^c \end{bmatrix} \begin{bmatrix} L_1^c \\ L_2^c \end{bmatrix}$$
 matrix-vector multiplication

transformation matrices of directional couplers



 L_2

MZI

 L'_1

MZI Network as Neural Network

• MZIs can be connected to transform more signals simultaneously

 $\mathbf{T} = \mathbf{T}_{C_4} \mathbf{T}_{C_3} \mathbf{T}_{C_2} \mathbf{T}_{C_1} : \text{multiplication of column matrices} \\ \text{formed from the matrices of MZIs}$



 Neural networks can be mapped onto MZI networks by matrix decomposition



Process Variations of MZIs

- Same thermal power results in different phase changes in different MZIs due to process variations
- Smaller MZI phases have smaller deviations.



Phase changes vs applied power: characteristic curves of five MZIs under process variations

Accuracy Degradation of Neural Networks due to Process Variations

- LeNet-5 + Cifar10
- Obvious accuracy drop with 0.5%–1% random variations in the MZI phases
- With beyond 3% variations the optical network becomes unusable.

 3σ : variation setting, μ : mean value of accuracy



Ying Zhu, Grace Li Zhang, Bing Li, Xunzhao Yin, Cheng Zhuo, Huaxi Gu, Tsung-Yi Ho, Ulf Schlichtmann. *Countering Variations and Thermal Effects for Accurate Optical Neural Networks*. ICCAD, 2020

Variation Extraction from MZI Network

- Input test pattern: Identity matrix I
 - $\rightarrow M = T_{C_4} T_{C_3} T_{C_2} T_{C_1} I$
- Change MZI phases in column four

 $\to M' = T_{C_4}' T_{C_3} T_{C_2} T_{C_1} I$

 Determine MZI variations by curve matching and columnwise iterative test



Accuracy Enhancement in Variation-aware Design



#sampled ONNs: 100; 3σ of the phases at 2π : 20%; Aug. LeNet-5: LeNet-5 with more convolutional layers

Ying Zhu, Grace Li Zhang, Bing Li, Xunzhao Yin, Cheng Zhuo, Huaxi Gu, Tsung-Yi Ho, Ulf Schlichtmann. Countering Variations and Thermal Effects for Accurate Optical Neural Networks. ICCAD, 2020

Future Challenges of Optical Systems

- Design and test of optical networks
 - Fault test
 - Variation characterization of complex MRR and MZI networks
- Fusion of optical interconnects and computing components
 - Optical interconnects can create test paths and enable fault tolerance.
 - Overlapped design allows more flexible MZI network structures.
- Computing in the optical domain
 - More functions can be integrated into the optical domain \rightarrow optic-electro conversion as late as possible
 - Codesign of optical and electrical systems







Tutorial IV: Integrated Programmable Photonic Circuits

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Zhengqi Gao, Duane S. Boning

Department of EECS, MIT

July 10th, San Francisco



Terminology



Figure credit: Wim Bogaerts et al., Nature, 2020.

- Contrast to bulk optics (which are individual, discretized)
- Integrate multiple optical functions onto a single chip
- Several platforms, mostly used: silicon-based CMOS

Integrated Programmable Photonic Circuit

- Active photonic devices (thermal-optic phase shifter)
- Exploit run-time reconfigurability
- Analogy to the concept of FPGA
- Manipulate light (EM wave), instead of electric signal
- Physical abstraction is $\{E, H\}$, instead of $\{I, V\}$.
- Simulation more complicated (PDE, Maxwell Equations)







Hardware side

Software side

FPGA Reconfigurability

- A large number of logic blocks (e.g., lookup tables, flip-flops, multiplexers)
- An interconnect routing network, which can be programmed
- Program FPGA with HDL (e.g., VHDL, Verilog)



What about an integrated programmable photonic circuit?







Hardware Side



Figure credit: Wim Bogaerts et al., Nature, 2020.

Tunable Basic Unit (TBU)

- An active 2x2 MZI device
- Two degrees of freedom
- Thermal/electric-optical phase shifters
- Three states: bar, cross, partial
- Several implementations (figs. c, d, e)

Remarks: (i) analog computing, (ii) topology difference





Reck's design



VOLUME 73, NUMBER 1 PHYSICAL REVIEW LETTERS 4 JUL

4 JULY 1994

Experimental Realization of Any Discrete Unitary Operator

Michael Reck and Anton Zeilinger Institut für Experimentalphysik, Universität Innsbruck, Technikerstrasse 25, A-6020 Innsbruck, Austria

> Herbert J. Bernstein and Philip Bertani Hampshire College and ISIS, Amherst, Massachusetts 01002 (Received 11 February 1994)

An algorithmic proof that any discrete finite-dimensional unitary operator can be constructed in the laboratory using optical devices is given. Our recursive algorithm factorizes any $N \times N$ unitary matrix into a sequence of two-dimensional beam splitter transformations. The experiment is built from the corresponding devices. This also permits the measurement of the observable corresponding to any discrete Hermitian matrix. Thus optical experiments with any type of radiation (photons, atoms, etc.) exploring higher-dimensional discrete quantum systems become feasible.

Remarks: (i) Reck's design could implement any complex unitary *N*-by-*N* matrix with *N*(*N*-1)/2 MZIs.(ii) Feedforward: light only propagate from left to right, or vice versa; no loops.







Clement's design



One MZI

Optimal design for universal multiport interferometers

WILLIAM R. CLEMENTS,* PETER C. HUMPHREYS, BENJAMIN J. METCALF, W. STEVEN KOLTHAMMER, AND IAN A. WALMSLEY

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Universal multiport interferometers, which can be programmed to implement any linear transformation between multiple channels, are emerging as a powerful tool for both classical and quantum photonics. These interferometers are typically composed of a regular mesh of beam splitters and phase shifters, allowing for straightforward fabrication using integrated photonic architectures and ready scalability. The current, standard design for universal multiport interferometers is based on work by Reck *et al.* [Phys. Rev. Lett. 73, 58 (1994)]. We demonstrate a new design for universal multiport interferometers based on an alternative arrangement of beam splitters and phase shifters, which outperforms that by Reck *et al.* Our design requires half the optical depth of the Reck design and is significantly more robust to optical losses.

Remarks: (i) Similar to Reck's: *N*(*N*-1)/2 MZIs needed; feedforward.

(ii) Difference: better tolerance to error; more compact.







Singular value decomposition (SVD): $M = U\Sigma V$

M is a complex (real) matrix => U and V are unitary (orthogonal) matrices

Motivates a novel DL hardware accelerator: Optical Neural Network



Figure credit: Yichen Shen et al., Nature, 2017.





LIGHTELLIGENCE

Lightmatter's photonic Al ambitions light up an \$80M B round

Devin Coldeway @techcrunch / 9.01 AM CDT • May 6, 2021







Q1: Let's implement an optical ring resonator on a feedforward mesh!

---- We cannot... No closed loops.

Q2: Let's implement an IIR filter on a feedforward mesh!

---- Again, we cannot... No closed loops.



Ring resonator



Remark: Feedforward mesh is thus more specialized as DL accelator.



For a general optical application?







Topology II: Recirculating Mesh (Main Focus)

Common realizations: Square, hexagonal, triangular mesh

A "photonic FPGA": Fast prototyping integrated silicon photonic circuits



Figure credit: Leimeng Zhuang et al., Optica, 2015.



Figure credit: Wim Bogaerts et al., Nature, 2020.







Go back to Software Side

- How do we program it?
 - Recall: mature tools for electronic FPGA; digital.
 - But for programmable photonic circuits, it's analog computing.
- Feedforward mesh (Reck's and Clement's) has analytical solution
- This tutorial will focus on recirculating mesh (less touched)
 - No analytical solution available
 - Take mathematical and algorithmical perspectives

Remark: In a nutshell, we are doing synthesis.







A time-harmonic chromatic optical signal is represented by: ae^{jwt} (*a* is complex)

Scattering matrix relation for a TBU









A time-harmonic chromatic optical signal is represented by: ae^{jwt} (*a* is complex)

Scattering matrix relation for a TBU



Remarks: (i) This is the form usually used in a feedforward case

(ii) But more careful treatment needs to be done in a recirculating case







A time-harmonic chromatic optical signal is represented by: ae^{jwt} (*a* is complex)

Scattering matrix relation for a TBU



$$\begin{bmatrix} b_1^{(O)} \\ b_2^{(O)} \end{bmatrix} = \underbrace{\frac{\sqrt{2}}{2} \begin{bmatrix} 1 & -j \\ -j & 1 \end{bmatrix}}_{\text{right DC}} \underbrace{\begin{bmatrix} e^{-j\theta} & 0 \\ 0 & e^{-j\phi} \end{bmatrix}}_{\text{PSs}} \underbrace{\frac{\sqrt{2}}{2} \begin{bmatrix} 1 & -j \\ -j & 1 \end{bmatrix}}_{\text{left DC}} \begin{bmatrix} a_1^{(I)} \\ a_2^{(I)} \end{bmatrix}$$

Further take waveguide into consideration:



 $\{\theta, \phi\}$: tunable phase shifts (design variable) c: light speed in vaccum $n_{\text{eff}}(w)$: effective index of propogating mode α : tunable basic unit (TBU) loss L: length of waveguide in the TBU







A time-harmonic chromatic optical signal is represented by: ae^{jwt} (*a* is complex)

Scattering matrix relation for a TBU



 $\begin{bmatrix} b_1^{(O)} \\ b_2^{(O)} \end{bmatrix} = \alpha e^{-j\omega \frac{n_{\text{eff}}L}{c}} \begin{bmatrix} 0 & 1 \\ 1 & 0 \end{bmatrix} \begin{bmatrix} a_1^{(I)} \\ a_2^{(I)} \end{bmatrix}$

Cross state: $\theta = \phi$ Example: $\theta = \phi = -0.5\pi$

Other cases are referred to as the partial state







A time-harmonic chromatic optical signal is represented by: ae^{jwt} (*a* is complex)

Scattering matrix relation for a TBU



Remark II: Why it doesn't matter in a feedforward case?



Credit: Saumil Bandyopadhyay et al., Optica, 2021.







A time-harmonic chromatic optical signal is represented by: ae^{jwt} (*a* is complex)

Scattering matrix relation for a TBU



Remark II: Why it doesn't matter in a feedforward case?









A time-harmonic chromatic optical signal is represented by: ae^{jwt} (*a* is complex)

Scattering matrix relation for a TBU



Remark III: Why it does matter in a recirculating case?















A time-harmonic chromatic optical signal is represented by: ae^{jwt} (*a* is complex)

Scattering matrix relation for a TBU

$$a_{1}^{(l)}e^{jwt} \xrightarrow{\text{Port } A_{1}} \theta \xrightarrow{\text{Port } B_{1}} b_{1}^{(0)}e^{jwt} \left[\begin{array}{c} b_{1}^{(O)} \\ b_{2}^{(I)} \end{array} \right] = \mathbf{F} \left[\begin{array}{c} a_{1}^{(I)} \\ a_{2}^{(I)} \end{array} \right] = \underbrace{\alpha \cdot e^{-j\omega \frac{n_{\text{eff}}L}{c}}}_{\text{waveguide}} \underbrace{\sqrt{2}}_{2} \left[\begin{array}{c} 1 & -j \\ -j & 1 \end{array} \right] \left[\begin{array}{c} e^{-j\theta} & 0 \\ 0 & e^{-j\phi} \end{array} \right] \underbrace{\sqrt{2}}_{2} \left[\begin{array}{c} 1 & -j \\ -j & 1 \end{array} \right] \left[\begin{array}{c} a_{1}^{(I)} \\ a_{2}^{(I)} \end{array} \right] = \underbrace{\alpha \cdot e^{-j\omega \frac{n_{\text{eff}}L}{c}}}_{\text{waveguide}} \underbrace{\sqrt{2}}_{1} \left[\begin{array}{c} 1 & -j \\ 0 & e^{-j\phi} \end{array} \right] \underbrace{\sqrt{2}}_{2} \left[\begin{array}{c} 1 & -j \\ -j & 1 \end{array} \right] \left[\begin{array}{c} a_{1}^{(I)} \\ a_{2}^{(I)} \end{array} \right]$$

Remark IV: TBU is a bi-directional device:



$$\begin{bmatrix} b_1^{(O)} \\ b_2^{(O)} \\ a_1^{(O)} \\ a_2^{(O)} \end{bmatrix} = \begin{bmatrix} \mathbf{F} & \mathbf{0} \\ \mathbf{0} & \mathbf{F} \end{bmatrix} \begin{bmatrix} a_1^{(I)} \\ a_2^{(I)} \\ b_1^{(I)} \\ b_2^{(I)} \end{bmatrix}$$

{ θ , ϕ }: tunable phase shifts (design variable) c: light speed in vaccum $n_{\text{eff}}(w)$: effective index of propogating mode α : tunable basic unit (TBU) loss L: length of waveguide in the TBU







Frequency-domain scattering matrix simulation



A system of linear equations!







Frequency-domain scattering matrix simulation



	$F_{11}^1 \\ F_{21}^1$	$a_1 + a_1 $	$F^1_{12}a_2$ $F^1_{22}a_2$	$a_2 - a_2 - a_2 - a_2$	$a_3 = 0$ $a_4 = 0$						
	F_{11}^{1}	$b_3 + .$	$F_{12}^{1}b_{4}$	4 - b	$_{1} = ($)					
	F_{21}^{1}	$b_3 + .$	$F_{22}^{1}b_{4}$	$1 - b_{1}$	$_{2} = 0$)					
									$\begin{vmatrix} a_1 \\ b_1 \end{vmatrix}$		۲.,٦
F_{11}^{1}	0	F_{12}^{1}	0	-1	0	0	0		a_2		$\begin{bmatrix} 0\\ 0 \end{bmatrix}$
F_{21}^{1}	0	F_{22}^{1}	0	0	-1	0	0	•••	b_2		
0	-1	0	0	0	$F_{11}^{_{1}}$	0	$F_{12}^{_{1}}$	•••	a_3	=	
0	0	0	-1	0	F_{21}^{1}	0	F_{22}^{1}	••••	b_3		
÷	÷	÷	÷	÷	:	÷	÷	·	a_4		
-								-	b_4		[u]
									:		







- Reasonable assumption: all TBUs in bar or cross states because of 'routing'.
- Example: How to route an optical signal from node A to node E? -- Fairly easy









- Reasonable assumption: all TBUs in bar or cross states because of 'routing'.
- Example: How to route an optical signal from node A to node E? -- Fairly easy



Recall the S-matrix of a TBU in bar/cross state:

$$\begin{bmatrix} b_1^{(O)} \\ b_2^{(O)} \end{bmatrix} = \alpha \mathrm{e}^{-\mathrm{j}\omega \frac{n_{\mathrm{eff}}L}{c}} \begin{bmatrix} 1 & 0 \\ 0 & -1 \end{bmatrix} \begin{bmatrix} a_1^{(I)} \\ a_2^{(I)} \end{bmatrix}$$
$$\begin{bmatrix} b_1^{(O)} \\ b_2^{(O)} \end{bmatrix} = \alpha \mathrm{e}^{-\mathrm{j}\omega \frac{n_{\mathrm{eff}}L}{c}} \begin{bmatrix} 0 & 1 \\ 1 & 0 \end{bmatrix} \begin{bmatrix} a_1^{(I)} \\ a_2^{(I)} \end{bmatrix}$$

The frequency response of is: $(\alpha e^{-j\omega \frac{n_{eff}L}{c}})^4$

'4' represents the number of TBUs the trajectory bypasses







- Reasonable assumption: all TBUs in bar or cross states because of 'routing'.
- Define Path length = #TBUs bypassed
- Analyzing path length is very important
 - It determines the frequency response (previous page)
 - Application I: *N* signals, goes through the programmable photonic circuit, maintaining phases
 - Realize *N* paths with the same path length.
 - Application II: Work as time delay element for filtering
 - Realize paths with length constructing arithmetic sequence, e.g., {1,3,5,7,...}.







Conclusion I (warm up)



TBUs = N(M + 1) + M(N + 1)# Configs = $2^{N(M+1) + M(N+1)}$ # floating nodes = 4N + 4M# non-floating nodes = 4NM# undirected optical path = 2N + 2M







Conclusion II: maximum path length = 4NM + 1



Intuition: a path starts and ends both at a floating node, with non-floating nodes in the middle.

Path length = #Nodes - 1

where #Nodes = 2 + #Non-floating Nodes

=> Max #Nodes = 4*NM* + 2

= Max Path length = 4NM + 1







Conclusion III: Is any path length x in [1, 4NM+1] realizable on a N-by-M square mesh? Unluckily, no....









Conclusion III: Is any path length *x* in [1, 4*NM*+1] realizable on a *N*-by-*M* square mesh?



Our findings:

If both N and M are even,

Any $x = 0, 1, 2 \pmod{4}$ in [1, 4NM+1] is realizable

If both *N* is even and *M* is odd,

Any *x* = 0, 1, 2 (mod 4) in [1, 4*NM*+1] is realizable

Any $x = 3 \pmod{4}$ in [2M + 1, 4NM + 1 - 2M] is realizable

Other cases....

Single path reliazability






Routing Analysis



Other questions when multiple paths considered:

- Recall there are (2N + 2M) paths in total, what are their sum and standard deviation?
- Given a *N*-by-*M* square mesh, and a desired path length *x*, how many paths could we realize?

Refer to: https://arxiv.org/abs/2306.12607







- Route several signals on this programmable photonic circuits?
 - Preliminary investigation published in the literature (See Aitor Lopez et al., OE, 2020)
 - Our view: still an open problem, missing strict analysis
 - Graph theory might be helpful
- Besides routing, we also want other functions, e.g., splitting, filtering, WDM
 - Most demos are hand crafted: X size goes up, realize several functions.
 - Can we automatically synthesize light processing function?
 - Use analytical synthesis? --- No closed form for recirculating structure









TBUs = N(M + 1) + M (N + 1)

Phase shifts = $2N(M + 1) + 2M(N + 1) \sim 4NM$

We want to adjust phase shifts, to realize a desired function.

Formulate as an optimization problem!

Challenge: high-dimensional space

Efficient solution: Gradient descent w/ analytical gradients







We do a simplification



Remark: We consider this simplified case, so that we could derive the transfer function analytically







V matrix: Scattering matrix relation for a vertical TBU











V matrix: Scattering matrix relation for a vertical TBU





April 12, 2022



H matrix: Scattering matrix relation for a horizontal TBU





April 12, 2022



Build the overall scattering matrix iteratively (the j-th to the j+1-th column)





=>





Build the overall scattering matrix iteratively (the 0-th to the M-th column)





=>





We know how to build matrix **T**, and all operations invovled are differentiable

Define input and output and use a cost function: CostLog

















- Local minimum is accpetable
- Random initialization doesn't impact the synthesized results much
- Even could realize two functions at the same time





Ref: Zhengqi Gao et al., Photonics Res. 2023. (highlighted as Editor's pick)







Online Demo

In previous page, we show how to derive gradients analyticall in a simplified square mesh

What about gradient calculation in any topology (hexagonal, triangular, even mix)?

=> A light-weight Python package, Spode, specialized for programmable photonic circuit



A <u>s</u>imulator with <u>prog</u>rammable <u>pho</u>tonics and <u>di</u>fferentiability <u>e</u>mphasis

https://colab.research.google.com/drive/1ILw5831IcmhHSIQOWGuc7vPmQEsNKoq?usp=sharing

Remark: The package is for ease of research; integrate simulation, visualization, circuit generator.







Discussions and Future Directions

- Impact of the photodetector
- Error cascading (See Saumil's Optica 2021 paper)
- Provable routing algorithm
- Hardware demonstration







Further Reading

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- 2. Wim Bogaerts et al., 'Programmable Phototonic Circuits,' *Nature*, 2020.
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